

#5/a
4/25/02
A. Smith

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kyu-Nam Lim
Assignee: Samsung Electronics Co., Ltd
Title: Input Buffer Circuit
Serial No.: 09/574,306 (9/97/99) Filing Date: October 24, 2001
Examiner: Nguyen, L. Group Art Unit: 2816
Docket No.: AB-984-1C US



San Jose, California
April 10, 2002

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RESPONSE TO OFFICE ACTION

Dear Sir:

This is a response to the January 11, 2002 Office Action which has a statutorily shortened period for response that ends April 11, 2002. Further examination and reconsideration are respectfully requested in view of the remarks set forth below.

IN THE CLAIMS

The following is a clean version of the entire set of pending claims. In accordance with 37 CFR § 1.121(c)(1)(ii), Attachment A provides marked up versions of the claims containing the newly introduced changes.

1. An input buffer circuit comprising:
 - a first inverting switch connected to a first input voltage and outputting a self bias signal;
 - a second inverting switch connected to a second input voltage and an output signal;
 - and
 - a gain control unit having a feedback loop for gain control and responding to the self bias signal and the output signal.

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